

	U	1	Document ID	Issue Date	Pages	Title
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5875314 A	19990223	11	Configurable connection fabric for providing serial backplanes with adaptive port/module bandwidth
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5774698 A	19980630	107	Multi-media serial line switching adapter for parallel networks and heterogeneous and homologous computer system
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5724352 A	19980303	32	Terabit per second packet switch having assignable multiple packet loss probabilities
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5724349 A	19980303	31	Terabit per second ATM packet switch having out-of-band control with multi casting
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5642349 A	19970624	31	Terabit per second ATM packet switch having distributed out-of-band control
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5612953 A	19970318	105	Multi-media serial line switching adapter for parallel networks and heterogeneous and homologous computer systems
7	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5544160 A	19960806	31	Terabit per second packet switch
8	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5537403 A	19960716	32	Terabit per second packet switch having distributed out-of-band control of circuit and packet switching communications
9	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5535373 A	19960709	107	Protocol-to-protocol translator for interfacing disparate serial network nodes to a common parallel switching network
10	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5166926 A	19921124	50	Packet address look-ahead technique for use in implementing a high speed packet switch
11	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5157654 A	19921020	51	Technique for resolving output port contention in a high speed packet switch
12	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5130984 A	19920714	54	Large fault tolerant packet switch particularly suited for asynchronous transfer mode (ATM) communication

	Current OR	Current XRef	Retrieval Classif	Inventor	S	C	P
1	710/317	370/386		Edholm, Philip K.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	712/1	370/366		Olnowich, Howard Thomas	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
3	370/388	340/825.5; 370/395.1; 370/413; 370/419		Cloonan, Thomas Jay et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4	370/390	370/395.3; 370/427; 370/432		Cloonan, Thomas Jay et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
5	370/360	370/427		Cloonan, Thomas Jay et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
6	370/367	370/389		Olnowich, Howard T.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
7	370/395.1	340/825.5; 370/413; 370/419		Cloonan, Thomas J. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
8	370/352	370/395.1; 370/419		Cloonan, Thomas J. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
9	703/25	370/466; 703/26; 703/27		Olnowich, Howard T.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
10	370/392	370/399		Cisneros, Arturo et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
11	370/414			Cisneros, Arturo	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
12	370/399	370/414		Cisneros, Arturo	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>